



PTO/SB/08A (10-01)  
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Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)		<b>Complete if Known</b>	
		Application Number	09/815,778
Sheet 1 of 7		Filing Date	March 24, 2001
		First Named Inventor	Sandeep Khanna
		Art Unit	2661
		Examiner Name	Not Yet Assigned
		Attorney Docket Number	NLM.P003

U.S. PATENT DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
CUD		US- 3,257,646	06-21-1966	Roth	
CUD		US- 3,353,159	11-14-1967	Lee, III	
CUD		US- 3,602,899	08-31-1971	Lindquist et al.	
CUD		US- 3,675,211	07-04-1972	Raviv	
CUD		US- 3,685,020	08-15-1972	Meade	
CUD		US- 3,868,642	02-25-1975	Sachs	
CUD		US- 4,112,502	09-05-1978	Scheuneman	
CUD		US- 4,244,033	01-06-1981	Hattori	
CUD		US- 4,472,805	09-18-1984	Wacyk et al.	
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Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
CUD		EP 0 267 612 A2	05-18-1988	Akita		
CUD		EP 0 381 249 A2	08-08-1990	Akashi		
CUD		EP 0 660 332 A1	06-28-1995	Montoye		
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CUD		EP 08167295 (Japan Abst)	06-25-1996	Hiroshi		
CUD		EP 08273376 (Japan Abst)	10-18-1996	Takeshi		
CUD		WO 98/12651	03-26-1998	Kemoke		
CUD		WO 99/31581	06-24-1999	Therkildsen		

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			Filing Date	March 24, 2001	
			First Named Inventor	Sandeep Khanna	
			Art Unit	2661	
			Examiner Name	Not Yet Assigned	
			Attorney Docket Number	NLM.P003	
Sheet	2	of	7		

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		Number - Kind Code <sup>2</sup> (if known)			
CLD		US- 4,903,234	02-20-1990	Sakaraba et al.	
CLD		us- 4,928,260	05-22-1990	Chuang et al.	
CLD		US- 4,958,377	09-18-1990	Takahashi	
CLD		us- 4,959,811	09-25-1990	Szczepanek	
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CLD		us- 5,319,763	06-07-1994	Ho et al.	
CLD		us- 5,383,146	01-17-1995	Threewitt	
CLD		us- 5,414,704	05-09-1995	Spinney	

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			Filing Date	March 24, 2001	
			First Named Inventor	Sandeep Khanna	
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Sheet	4	of	7		

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Substitute for form 1449B/PTO		<b>Complete if Known</b>			
		Application Number	09/815,778		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Filing Date	March 24, 2001		
		First Named Inventor	Sandeep Khanna		
		Group Art Unit	2661		
		Examiner Name	Not Yet Assigned		
Sheet	5	of	7	Attorney Docket Number	NLM.P003

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
CD		Advanced Micro Devices, "Final Am99C10A 256 x 48 Content Addressable Memory" Publication No. 08125, Rev. G, December 1992, pp. 1-21.	
CD		MASAO AKATA, "A Scheduling Content-Addressable Memory for ATM Space-Division Switch Control", IEEE International Solid-State Circuits Conference, Feb. 1991, New York.	
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CD		PANKAJ GUPTA and NICK MCKEOWN, "Packet Classification on Multiple Fields", SIGCOMM (September 2, 1999), 14 pages.	
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CD		HIROSHI KADOTA et al., "An 8-kbit Content-Addressable and Reentrant Memory", IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 5, October 1985, pp. 951-956.	

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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
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CLD		KLSI KE5B064A1 Address Processor Ver. 2.0.2 Preliminary (believed to be published late 1995 or early 1996).	
CLD		KLSI KE5B064H Series Address Processor Ver. 1.0.1 (believed to be published in late 1995 or early 1996).	
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CLD		MUSIC Semiconductors, "MU9C1480 LANCAM Handbook", November 1994.	
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CLD		MUSIC Semiconductors, Advance Information, "MUAA Routing CoProcessor (RCP) Family", (October 1, 1998), Rev. 1b, pages 1-16.	
CLD		TAKESHI OGURA et al., "A 4-kbit Associative Memory LSI", IEEE Journal of Solid-State Circuit, Vol. SC-20, No. 6, December 1985, pp. 1277-1281.	
CLD		SERGIO R. RAMIREZ-CHAVEZ, "Encoding Don't Cares in Static and Dynamic Content-Addressable Memories", Transaction Briefs, IEEE Transactions on Circuits and System-II: Analog and Digital Signal Processing, Vol. 39, No. 8, August 1992, pp. 575-578.	
CLD		IAN N. ROBINSON, "Pattern Addressable Memory", IEEE Micro, June 1992, pp. 20-30.	
CLD		YONG-CHUI SHIN et al., "A Special-Purpose Content Addressable Memory Chip for Real-Time Image Processing", IEEE Journal of Solid-State Circuits, Vol. 27, No. 5, May 1992, pp. 737-744.	
CLD		Stardust.com, "WhitePaper -- Introduction to QoS Policies", 1999, pp. 1-22.	
CLD		V. SRINIVASAN, et al., "Packet Classification Using Tuple Space Search", SIGCOMM (September 2, 1999), 12 pages.	
CLD		KEIKICHI TAMARU, "The Trend of Functional Memory Development", INVITED PAPER Special Issue on LSI Memories, IEICE Trans. Electron, Vol. E76-C, No. 11, Nov. 1993, pp. 1545-1554.	
CLD		XIPENG XIAO and LIONEL M. NI, "Internet QoS: A Big Picture", IEEE Network, March/April 1999, pp. 8-18.	

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